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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Mac Apodaca et al.

§ Group Art Unit: 2812

Serial No.: 10/036,833

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Examiner:

Filed: December 31, 2001

For: Adhesion Layer For A Polymer
Memory Device And Method
Therefor

Atty. Dkt. No.: ITO.0016US
(P13085)

POWER OF ATTORNEY BY ASSIGNEE

Under the provisions of 37 C.F.R. § 3.71, the undersigned assignee of record of the entire interest in the above-identified patent/patent application by virtue of an assignment recorded (check as applicable):

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Date Recorded April 11, 2002

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elects to conduct the prosecution of the application/maintenance of the patent to the exclusion of the inventor(s). The undersigned hereby declares that he has reviewed the above-referenced assignment and hereby declares that, to the best of his knowledge, title is in the Assignee, and further declares that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true. The assignee hereby revokes any previous powers of attorney and appoints the following to prosecute this application/maintain this patent and transact all business in the Patent and Trademark Office connected therewith:

Timothy N. Trop
Reg. No. 28,994

Fred G. Pruner, Jr.
Reg. No. 40,779

Dan C. Hu
Reg. No. 40,025

Howard R. Boyle
Reg. No. 29,617

Mark J. Rozman
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Stephen D. Dellett
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Steven D. Yates Reg. No. 42,242	Calvin E. Wells Reg. No. 43,256	Michael Willardson Reg. No. 50,856	Charles K. Young Reg. No. 39,435

The undersigned is authorized to sign this statement on behalf of the Assignee.

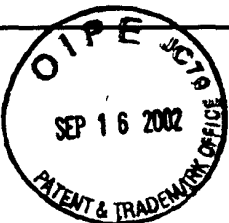
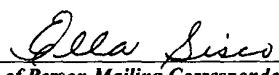
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(713) 468-8883 [Fax]

Please direct all telephone calls to: Timothy N. Trop at (713) 468-8880.

ASSIGNEE
Intel Corporation

Date: 8-26-02 BY: Thomas C. Reynolds
Thomas C. Reynolds
Director of Patents
Intel Corporation
2200 Mission College Blvd.
Santa Clara, CA 95054-1549

CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8) Applicant(s): Mac Apodaca et al.			Docket No. ITO.0016US
Serial No. 10/036,833	Filing Date December 31, 2002	Examiner	Group Art Unit 2812
Invention: Adhesion Layer For A Polymer Memory Device And Method Therefor			
<div style="text-align: center; margin-bottom: 20px;">  </div> <p>I hereby certify that this <u>Power of Attorney by Assignee (Intel Corporation)</u> <small>(Identify type of correspondence)</small></p> <p>is being deposited with the United States Postal Service as first class mail in an envelope addressed to: The Commissioner of Patents and Trademarks, Washington, D.C. 20231-0001 on <u>September 10, 2002</u> <small>(Date)</small></p> <div style="text-align: right; margin-top: 40px;"> <u>Ella Sisco</u> <small>(Typed or Printed Name of Person Mailing Correspondence)</small> <u></u> <small>(Signature of Person Mailing Correspondence)</small> </div>			
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Docket No.

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Serial No.

10/036,833

Filing Date

December 31, 2002

Examiner

Group Art Unit

2812

Invention: Adhesion Layer For A Polymer Memory Device And Method Therefor



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